



PE10N65J N-Channel Enhancement Mode Power MOSFET

PE10N65J Description

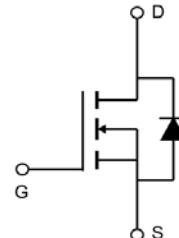
The PE10N65J uses deep trench technology to provide excellent $R_{DS(ON)}$ and low gate charge. It can be used in a wide variety of applications.

PE10N65J General Features

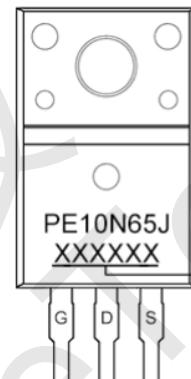
- $V_{DS} = 650V$, $I_D = 10A$
- $R_{DS(ON)} < 0.8\Omega$ @ $V_{GS}=10V$
- High Power and current handing capability
- Lead free product is acquired

PE10N65J Application

- PWM applications
- Load switch
- Power management



Schematic diagram



Marking and pin assignment



TO-220F

PE10N65J Absolute Maximum Ratings (TC=25°C unless otherwise noted)

| Parameter | Symbol | Rating | Unit |
|--|------------------------|------------|------|
| Drain-Source Voltage | V_{DS} | 650 | V |
| Gate-Source Voltage | V_{GS} | ± 30 | V |
| Drain Current-Continuous | I_D | 10 | A |
| Drain Current-Continuous ($T_c=100^\circ C$) | $I_D(T_c=100^\circ C)$ | 6.3 | A |
| Pulsed Drain Current (Note 1) | I_{DM} | 40 | A |
| Maximum Power Dissipation | P_D | 26 | W |
| Single Pulsed Avalanche Energy ($L=9.6mH$) | E_{AS} | 480 | mJ |
| Operating Junction and Storage Temperature Range | T_J, T_{STG} | -55 To 150 | °C |

PE10N65J Thermal Characteristic

| | | | |
|--|-----------------|------|------|
| Thermal Resistance, Junction-to-Case | $R_{\theta JC}$ | 4.8 | °C/W |
| Thermal Resistance, Junction-to-Ambient (Note 2) | $R_{\theta JA}$ | 43.6 | °C/W |



PE10N65J Electrical Characteristics (TC=25°C unless otherwise noted)

| Parameter | Symbol | Condition | Min | Typ | Max | Unit |
|---|--------------|--------------------------------------|-----|------|-----------|----------|
| Off Characteristics | | | | | | |
| Drain-Source Breakdown Voltage | BV_{DSS} | $V_{GS}=0V, I_D=250\mu A$ | 650 | - | - | V |
| Zero Gate Voltage Drain Current | I_{DSS} | $V_{DS}=650V, V_{GS}=0V$ | - | - | 1 | μA |
| Gate-Body Leakage Current | I_{GSS} | $V_{GS}=\pm 30V, V_{DS}=0V$ | - | - | ± 100 | nA |
| On Characteristics (Note 3) | | | | | | |
| Gate Threshold Voltage | $V_{GS(th)}$ | $V_{DS}=V_{GS}, I_D=250\mu A$ | 2 | 3 | 4 | V |
| Drain-Source On-State Resistance | $R_{DS(ON)}$ | $V_{GS}=10V, I_D=5A$ | - | 0.58 | 0.8 | Ω |
| Dynamic Characteristics (Note 4) | | | | | | |
| Input Capacitance | C_{iss} | $V_{DS}=25V, V_{GS}=0V, F=1.0MHz$ | - | 1187 | - | pF |
| Output Capacitance | C_{oss} | | - | 165 | - | pF |
| Reverse Transfer Capacitance (Note 4) | C_{rss} | | - | 4.2 | - | pF |
| Switching Characteristics | | | | | | |
| Turn-on Delay Time | $t_{d(on)}$ | $V_{DS}=335V, I_D=10A, R_G=25\Omega$ | - | 14 | - | nS |
| Turn-on Rise Time | t_r | | - | 28 | - | nS |
| Turn-Off Delay Time | $t_{d(off)}$ | | - | 62 | - | nS |
| Turn-Off Fall Time | t_f | | - | 35 | - | nS |
| Total Gate Charge | Q_g | $V_{DS}=536V, I_D=10A, V_{GS}=10V$ | - | 25 | - | nC |
| Gate-Source Charge | Q_{gs} | | - | 5.2 | - | nC |
| Gate-Drain Charge | Q_{gd} | | - | 9.8 | - | nC |
| Drain-Source Diode Characteristics | | | | | | |
| Diode Forward Voltage (Note 3) | V_{SD} | $V_{GS}=0V, I_S=10A$ | - | - | 1.2 | V |
| Diode Forward Current | I_S | $I_F=10A, dI/dt=100A/\mu s$ | - | - | 10 | A |
| Body Diode Reverse Recovery Time | t_{rr} | | - | 383 | - | ns |
| Body Diode Reverse Recovery Charge | Q_{rr} | | - | 3.2 | - | μC |

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to product.



PE10N65J Typical Electrical and Thermal Characteristics

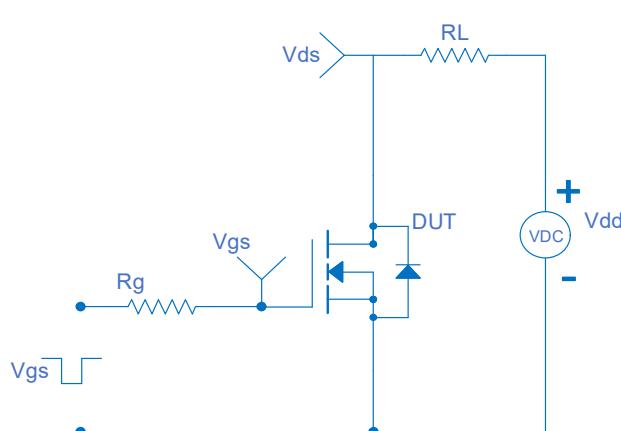


Figure 1 Switching Test Circuit

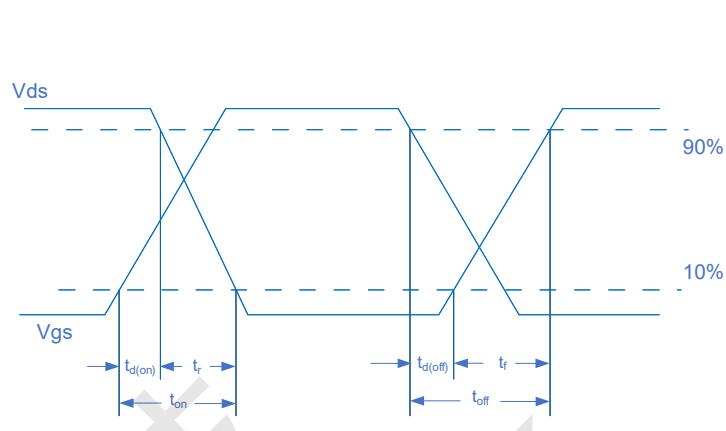


Figure 2 Switching Waveform

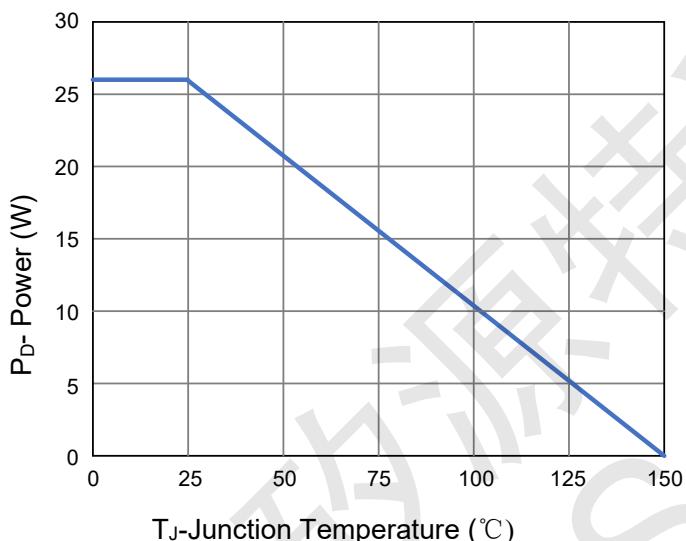


Figure 3 Power De-rating

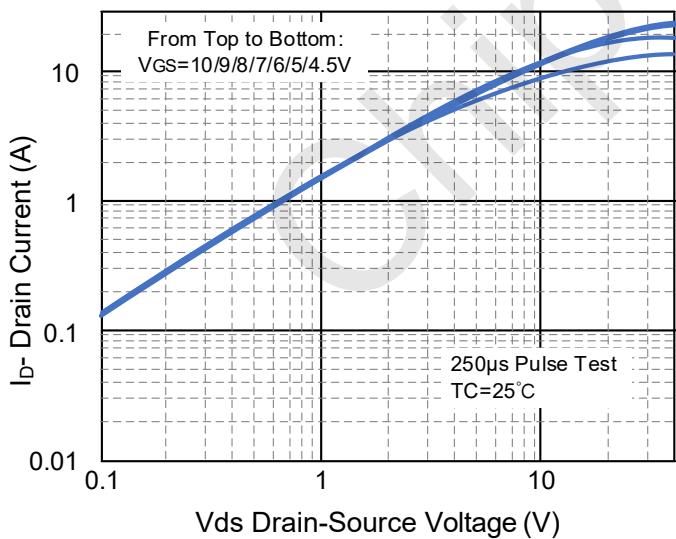


Figure 5 Output Characteristics

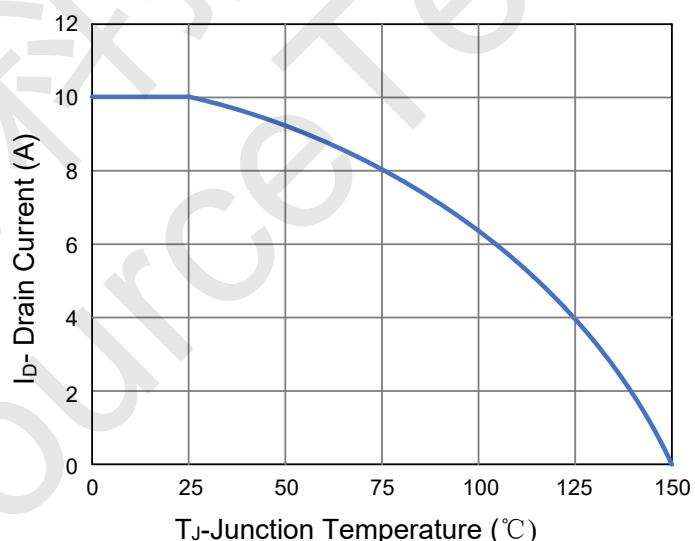


Figure 4 Drain Current

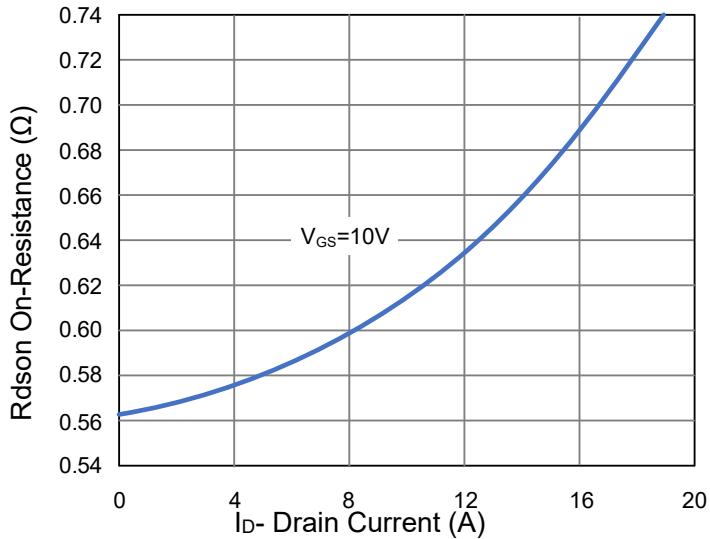


Figure 6 Rdson vs Drain Current

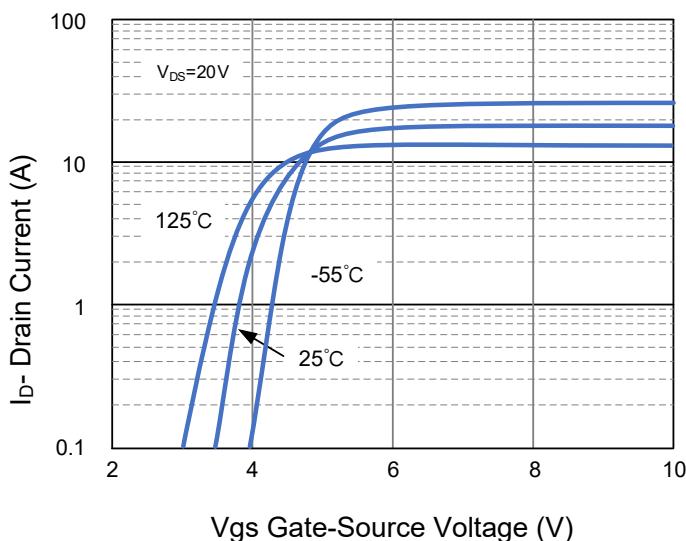


Figure 7 Transfer Characteristics

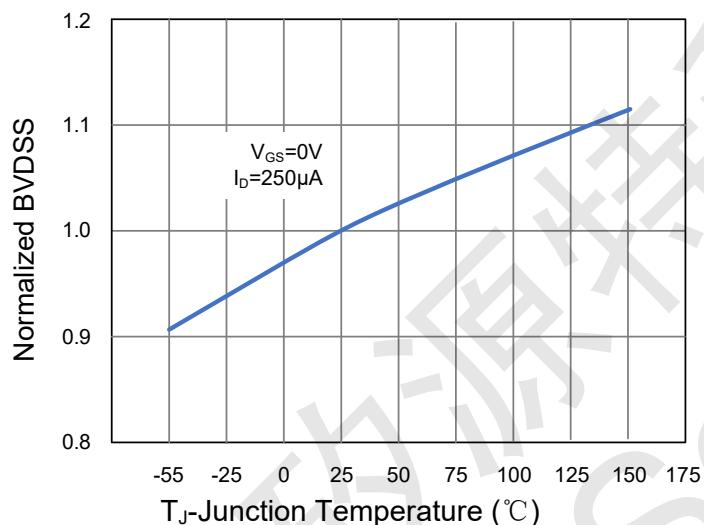


Figure 9 BVDSS vs Junction Temperature

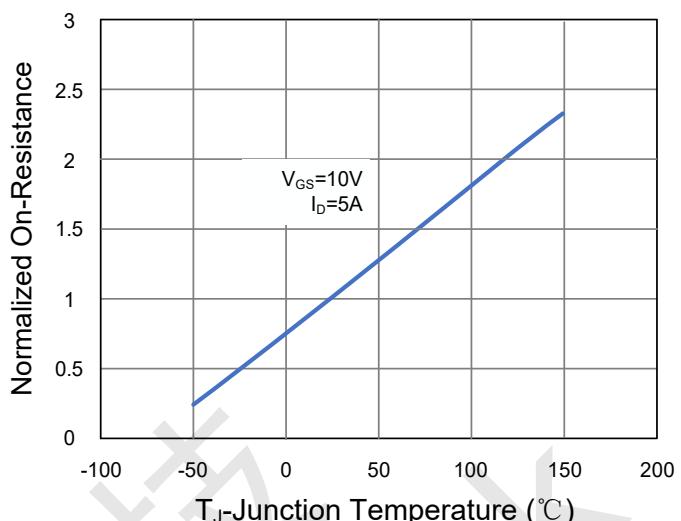


Figure 8 R_{DSON} vs Junction Temperature

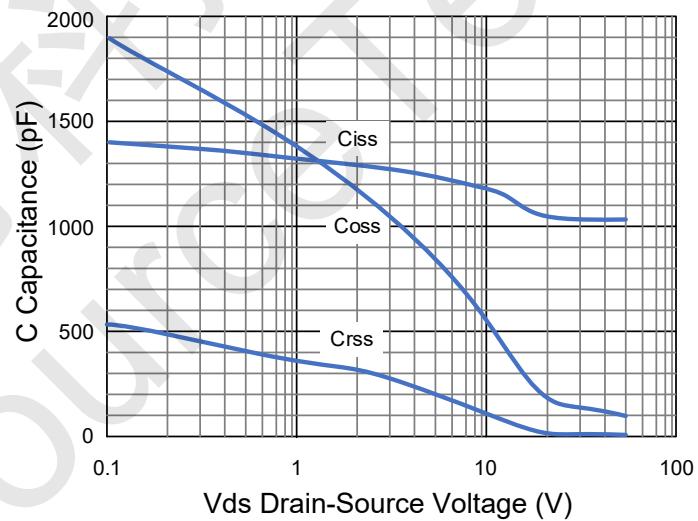


Figure 10 Capacitance vs V_{DS}

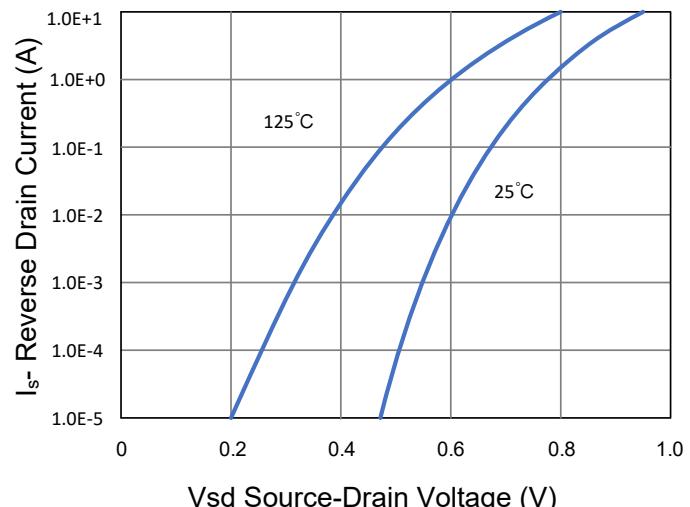


Figure 11 Gate Charge

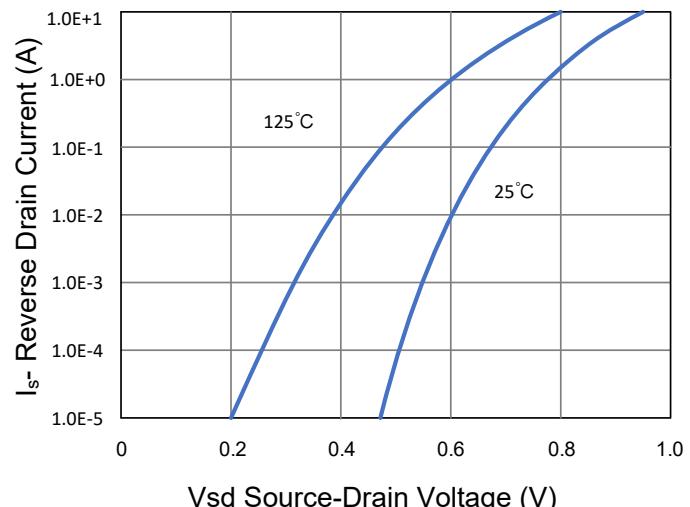


Figure 12 Source-Drain Diode Forward

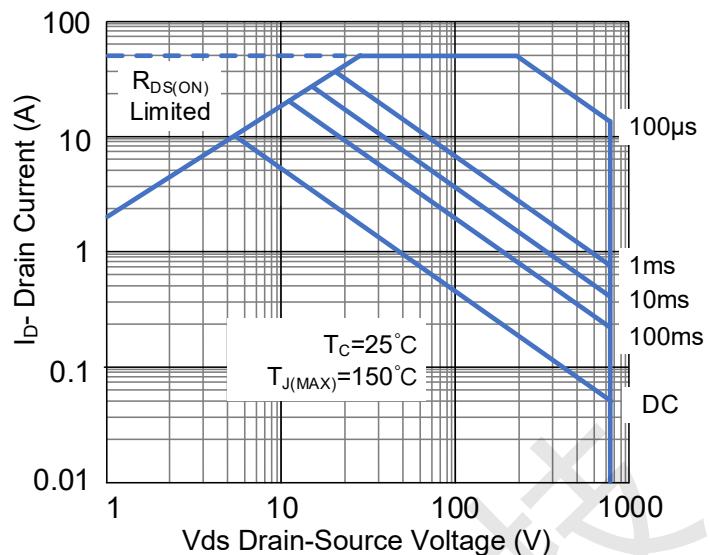


Figure 13 Safe Operation Area

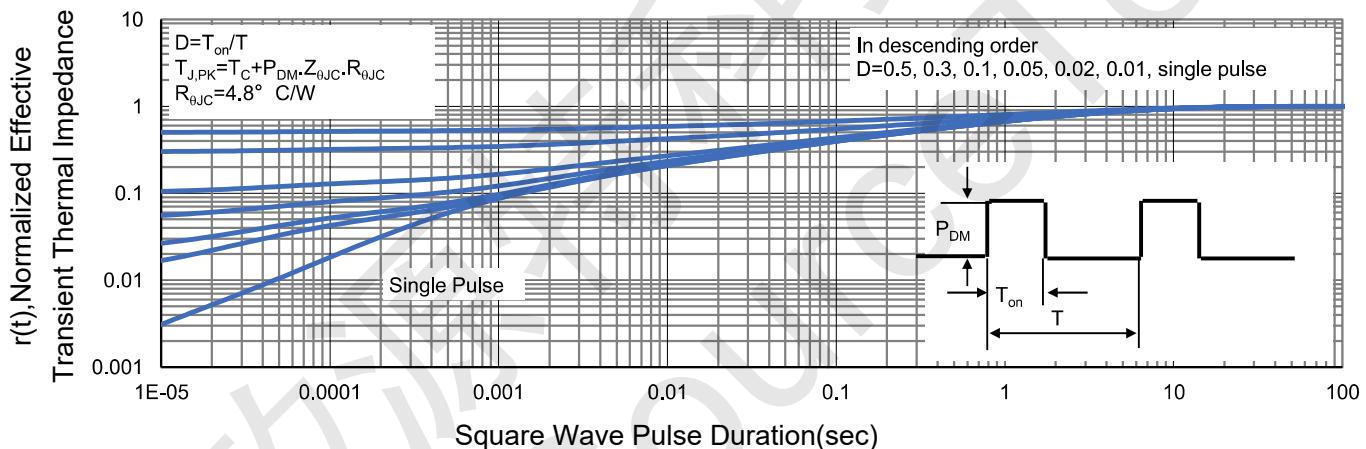
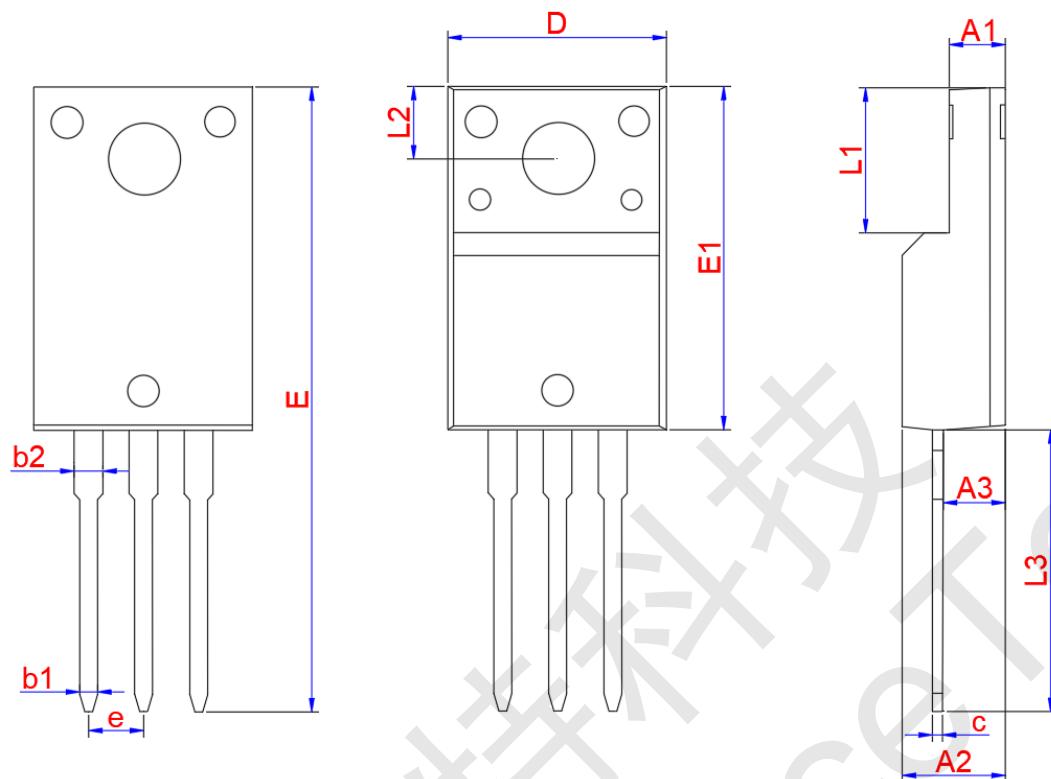


Figure 14 Normalized Maximum Transient Thermal Impedance



PE10N65J TO-220F Package Information



| Symbol | Dimensions In Millimeters | | |
|--------|---------------------------|--------|--------|
| | Min. | Typ. | Max. |
| A1 | 2.440 | 2.540 | 2.640 |
| A2 | 4.600 | 4.700 | 4.800 |
| A3 | 2.730 | 2.830 | 2.930 |
| b1 | 0.750 | 0.800 | 0.850 |
| b2 | 1.230 | 1.280 | 1.330 |
| c | 0.450 | 0.500 | 0.550 |
| D | 10.060 | 10.160 | 10.260 |
| E | 28.650 | 28.850 | 29.050 |
| E1 | 15.770 | 15.870 | 15.970 |
| e | 2.54 TYP. | | |
| L1 | 6.68 REF | | |
| L2 | 3.30 REF | | |
| L3 | 12.830 | 12.980 | 13.130 |