P-Channel Enhancement Mode Power MOSFET **MX6005S**

DESCRIPTION

The MX6005S uses advanced trench technology to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

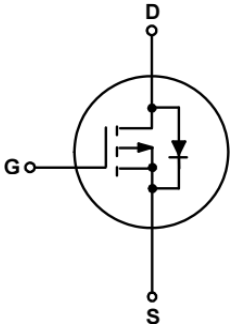
GENERAL FEATURES

- V_{DS}=-60V, I_D=-5A
  $R_{DS(ON)}$(Typ.)=53mΩ @ V_{GS}=-10V
  $R_{DS(ON)}$(Typ.)=69mΩ @ V_{GS}=-4.5V
- Surface-mounted package
- Low gate change

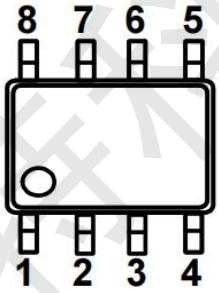
APPLICATION

- Motor driver appliances
- High power inverter system
- Adapter appliances

PINOUT



Schematic diagram



SOP8 top view

Pin	Description
1,2,3	Source(S)
4	Gate(G)
5,6,7,8	Drain(D)

ORDERING INFORMATION

Part Number	StorageTemperature	Package	Devices Per Reel
MX6005S	-55°C to 150°C	SOP8	3000

ABSOLUTE MAXIMUM RATINGS(T_A=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	-60	V
Gate-Source Voltage	V_{GS}	±20	V
Drain Current(V_{GS}=-10V)(Note1)	I_D	-5	A
Drain Current(T_A=100°C)(V_{GS}=-10V)(Note1)	I_D	-2.9	A
Pulsed Drain Current(V_{GS}=-10V)(Note1)(Note2)	I_{DM}	-18	A
Total Power Dissipation(Note1)	P_{tot}	2	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	°C
Thermal Resistance, Junction-to-Ambient(Note1)	$R_{\theta JA}$	3.5	°C/W
Thermal Resistance, Junction-to-Case(Note1)	$R_{\theta JC}$	62.5	°C/W

Note 1. Surface Mounted on 1 in^2 pad area, t ≤ 10 sec
Note 2. Pulse width ≤ 300μs, duty cycle ≤ 2%

ELECTRICAL CHARACTERISTICS(T_A=25°C unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V, I_{DS}=-250μA	-60	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=-48V, V_{GS}=0V	-	-	-1	μA
Gate-Body Leakage Current	I_{GSS}	V_{GS}=±20V, V_{DS}=0V	-	-	±100	nA
On Characteristics						
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_{DS}=-250μA	-1.0	-	-2.5	V
Drain-Source On-State Resistance(Note1)	$R_{DS(ON)}$	V_{GS}=-10V, I_D=-3A	-	53	60	mΩ
		V_{GS}=-4.5V, I_D=-2A	-	69	85	mΩ
Dynamic Characteristics(Note2)						
Input Capacitance	C_{iss}	V_{DS}=-30V, V_{GS}=0V, F=1.0MHz	-	1480	-	pF
Output Capacitance	C_{oss}		-	63	-	pF
Reverse Transfer Capacitance	C_{rss}		-	54	-	pF
Switching Characteristics(Note2)						
Turn-on Delay Time	$t_{d(on)}$	V_{DS}=-30V, I_{DS}=-3A, V_{GEN}=-10V, R_G=3.9Ω, R_L=10Ω,	-	5.5	-	nS
Turn-on Rise Time	t_r		-	3.7	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	102	-	nS
Turn-Off Fall Time	t_f		-	38	-	nS
Total Gate Charge	Q_g	V_{DS}=-30V, I_{DS}=-3A, V_{GS}=-10V	-	25	-	nC
Gate-Source Charge	Q_{gs}		-	6.5	-	nC
Gate-Drain Charge	Q_{gd}		-	3.4	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage(Note1)	V_{SD}	V_{GS}=0V, I_{SD}=-3A	-	-	-1.3	V
Reverse Recovery Time	t_{rr}	I_{SD}=-3A, dI_{SD}/dt=100A/μs	-	15	-	nS
Reverse Recovery Charge	Q_{rr}		-	12	-	nC

Note 1. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
Note 2. Guaranteed by design, not subject to prduction testing

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

Figure 1. Power Dissipation

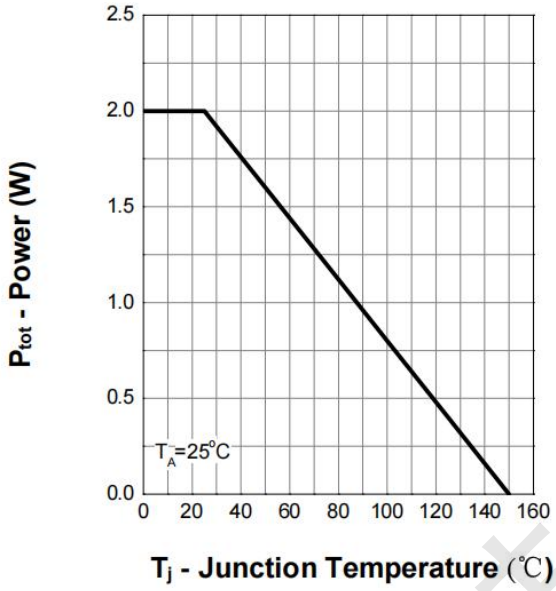


Figure 2. Drain Current

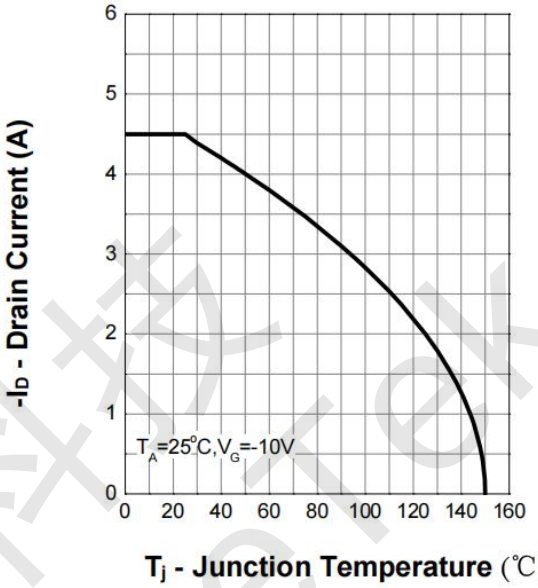


Figure 3. Safe Operation Area

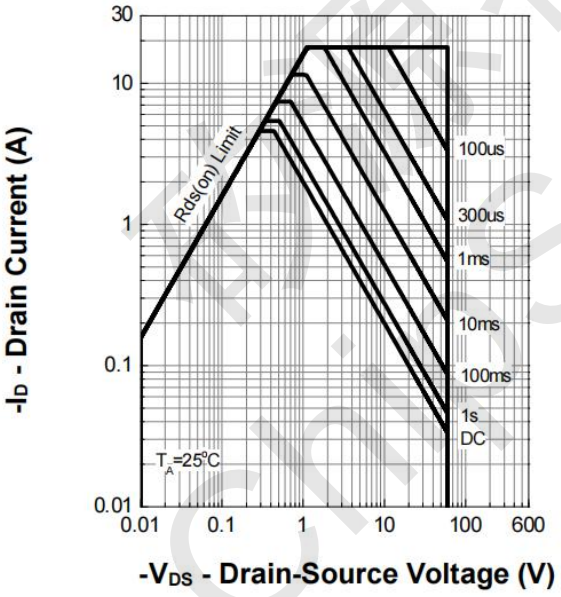
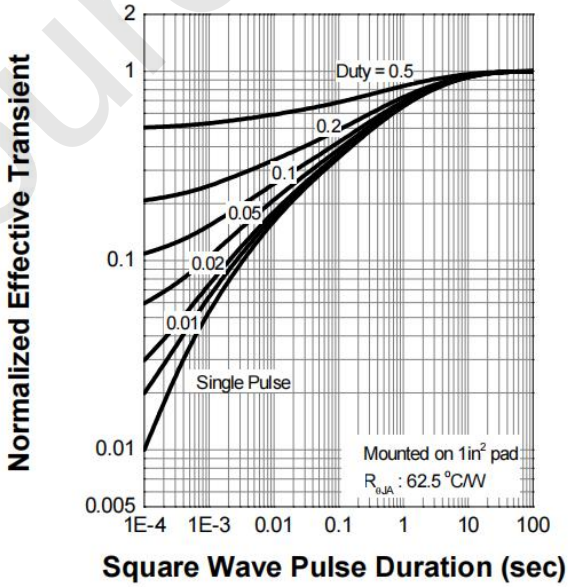


Figure 4. Transient Thermal Impedance



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

Figure 5. Output Characteristics

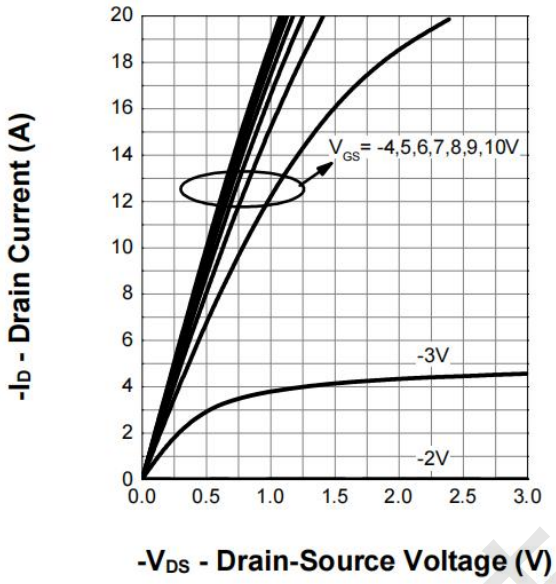


Figure 6. On Resistance

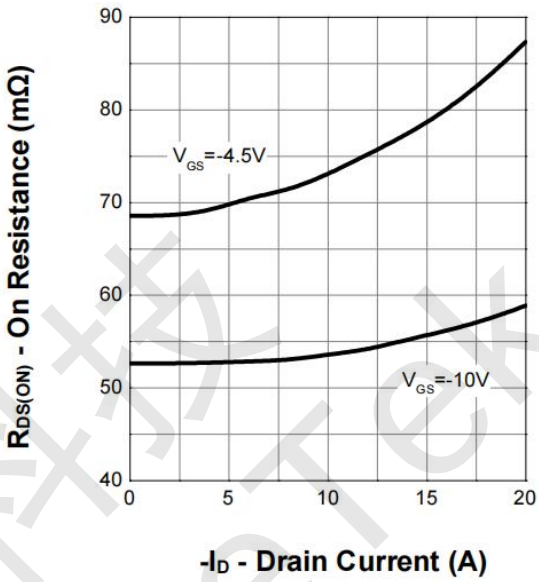


Figure 7. Transfer Characteristics

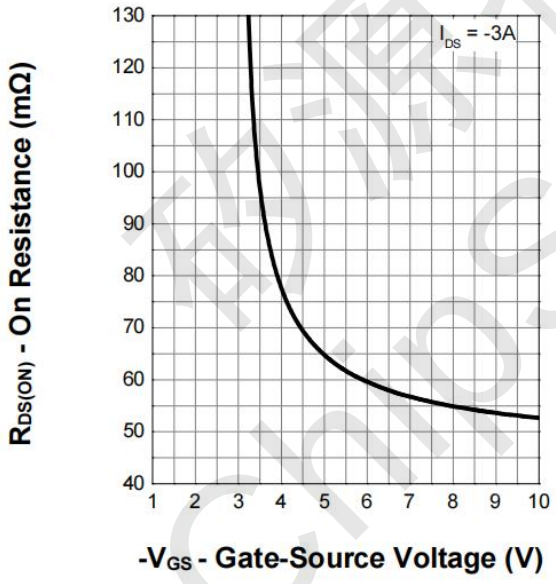
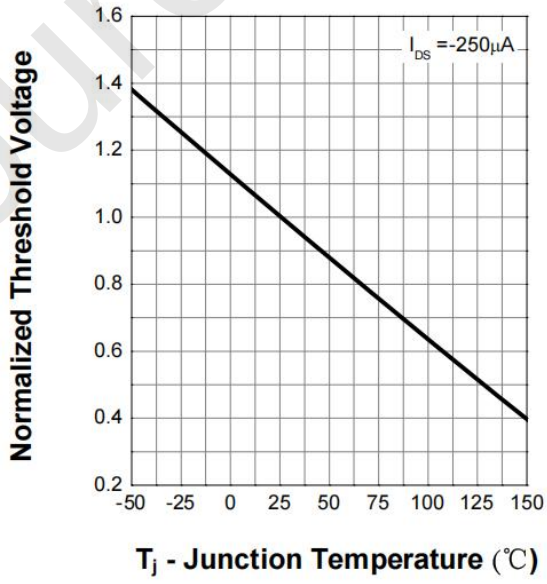


Figure 8. Normalized Threshold Voltage



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

Figure 9. Normalized On Resistance

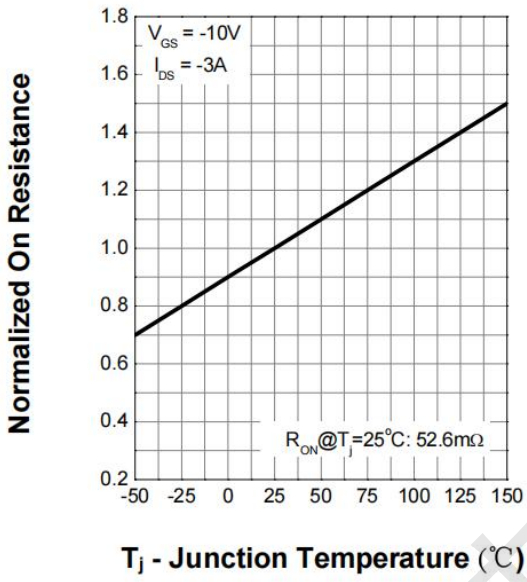


Figure 10. Diode Forward Current

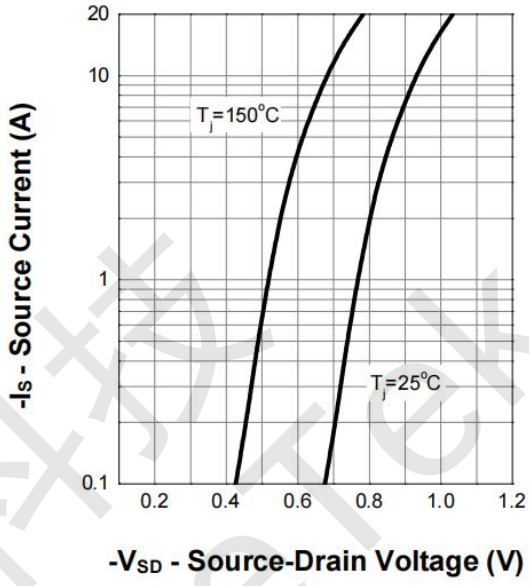


Figure 11. Capacitance

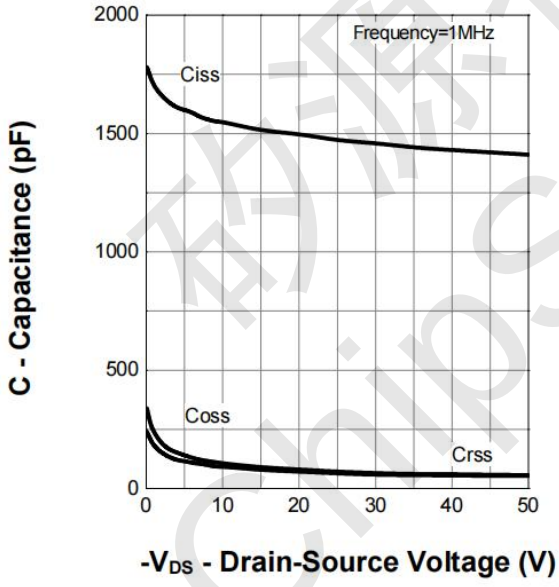
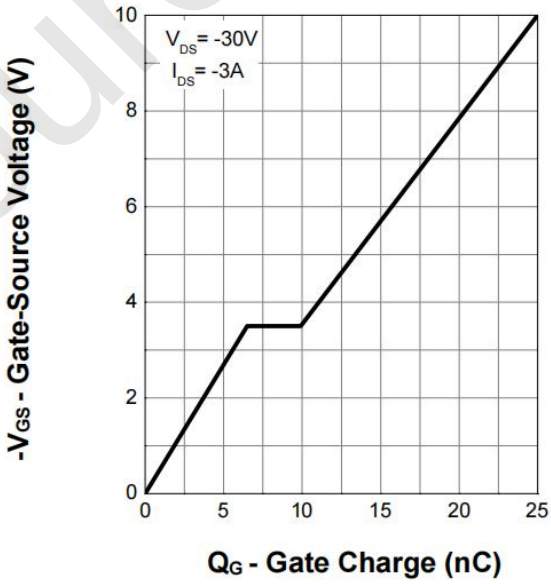
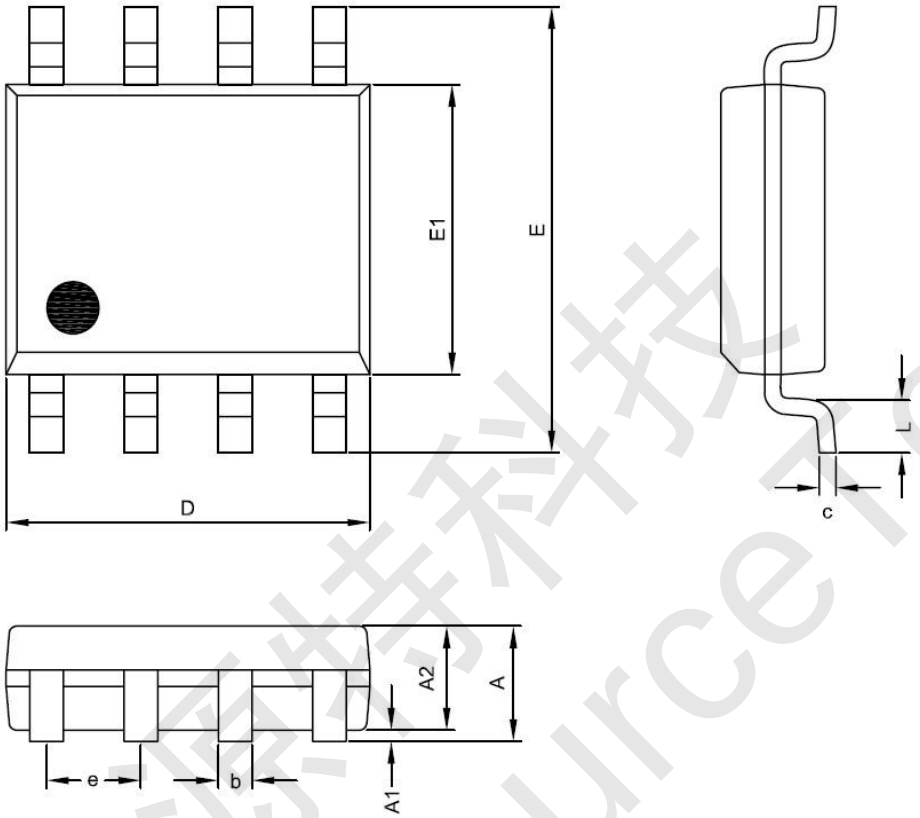


Figure 12. Gate Charge



PACKAGE INFORMATION

SOP-8L



Symbol	Dimensions In Millimeters	
	MIN.	MAX.
A	1.35	1.75
A1	0.00	0.25
A2	1.15	1.50
D	4.80	5.00
E	5.80	6.20
E1	3.80	4.00
c	0.19	0.27
b	0.33	0.53
e	1.27 BSC	
L	0.40	1.27